



**JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
HYDERABAD - 500 085, ANDHRA PRADESH, INDIA**

CONSOLIDATED MARKS MEMO / CREDIT SHEET



CMM. No. : **C 0473761**

BACHELOR OF TECHNOLOGY- ELECTRONICS & COMMUNICATION ENGINEERING

Serial No : 21101071433

Name of the College : 93-K T R M C E, KONDER

Name : **K NARESH KUMAR**

Month & Year of Final Exam : April, 2014

Hall Ticket No. : 10931A0442

Year of Admission : 2010-2011

Class Awarded : **FIRST CLASS**

S.No.	SUBJECT TITLE	INT MARKS	EXT MARKS	TOTAL	CREDITS	S.No.	SUBJECT TITLE	INT MARKS	EXT MARKS	TOTAL	CREDITS
I YEAR											
1	ENGLISH	19	43	62	4	2	ENGLISH LANGUAGE COMMUNICATION SKILLS LAB	23	48	71	4
3	IT WORKSHOP & ENGINEERING WORKSHOP	22	29	51	4	4	MATHEMATICS - I	18	32	50	6
5	MATHEMATICAL METHODS	19	34	53	6	6	ENGINEERING PHYSICS	16	51	67	4
7	ENGINEERING CHEMISTRY	16	35	51	4	8	COMPUTER PROGRAMMING & DATA STRUCTURES	17	40	57	6
9	ENGINEERING DRAWING	22	26	48	4	10	COMPUTER PROGRAMMING LAB	21	34	55	4
11	ENGG. PHYSICS & ENGG. CHEMISTRY LAB	23	46	69	4						
I SEMESTER											
I SEMESTER						II SEMESTER					
1	MATHEMATICS-III	19	47	66	3	1	PRINCIPLES OF ELECTRICAL ENGINEERING	19	66	85	3
2	PROBABILITY THEORY & STOCHASTIC PROCESSES	21	46	67	3	2	ELECTRONIC CIRCUIT ANALYSIS	17	39	56	4
3	ENVIRONMENTAL STUDIES	20	62	82	3	3	PULSE & DIGITAL CIRCUITS	20	30	50	4
4	ELECTRIC CIRCUITS	18	38	56	4	4	SWITCHING THEORY & LOGIC DESIGN	19	34	53	4
5	ELECTRONIC DEVICES & CIRCUITS	18	30	48	4	5	ELECTROMAGNETIC THEORY & TRANSMISSION LINES	20	34	54	4
6	SIGNALS & SYSTEMS	22	54	76	4	6	ELECTRICAL ENGINEERING LAB	23	47	70	2
7	ELECTRONIC DEVICES & CIRCUITS LAB	22	33	55	2	7	ELECTRONIC CIRCUIT ANALYSIS LAB	23	45	68	2
8	BASIC SIMULATION LAB	21	41	62	2	8	PULSE & DIGITAL CIRCUITS LAB	22	46	68	2
II SEMESTER											
I SEMESTER						II SEMESTER					
III YEAR											
1	CONTROL SYSTEMS	22	57	79	4	1	MANAGERIAL ECONOMICS & FINANCIAL ANALYSIS	23	29	52	4
2	COMPUTER ORGANIZATION	19	50	69	4	2	ADVANCED ENGLISH COMMUNICATION SKILLS (LAB)	24	48	72	2
3	ANTENNAS & WAVE PROPAGATION	21	49	70	3	3	OPERATING SYSTEMS	15	26	41	4
4	ELECTRONIC MEASUREMENTS & INSTRUMENTATION	21	29	50	4	4	DIGITAL COMMUNICATIONS	19	28	47	3
5	ANALOG COMMUNICATIONS	16	40	56	3	5	MICROPROCESSORS & MICROCONTROLLERS	19	48	67	4
6	IC APPLICATIONS	18	40	58	3	6	DIGITAL SIGNAL PROCESSING	18	47	65	4
7	ANALOG COMMUNICATIONS (LAB)	24	46	70	2	7	MICROPROCESSORS & MICROCONTROLLERS (LAB)	22	45	67	2
8	IC APPLICATIONS (LAB)	21	41	62	2	8	DIGITAL SIGNAL PROCESSING (LAB)	20	44	64	2
II SEMESTER											
I SEMESTER						II SEMESTER					
IV YEAR											
1	MANAGEMENT SCIENCE	20	36	56	3	1	CELLULAR & MOBILE COMMUNICATIONS	15	26	41	3
2	EMBEDDED SYSTEMS	19	13	32	0	2	WIRELESS COMMUNICATIONS & NETWORKS	22	28	50	3
3	E-CAD AND VLSI LAB	23	49	72	2	3	INDUSTRY ORIENTED MINI PROJECT	-	45	45	2
4	MICROWAVE ENGG. AND DIGITAL COMMNS. LAB	21	45	66	2	4	SEMINAR	48	-	48	2
5	VLSI DESIGN	16	30	46	4	5	MAJOR PROJECT	50	146	196	10
6	MICROWAVE ENGINEERING	21	29	50	3	6	COMPREHENSIVE VIVA	-	94	94	2
7	COMPUTER NETWORKS	21	26	47	4	7	RADAR SYSTEMS	24	40	64	3
8	DIGITAL IMAGE PROCESSING	24	41	65	3						

(# Project Internal= 50, External=150)

Number of Credits registered for : 200 Aggregate Marks Secured for best: 192

Aggregate Marks Secured **3538 OUT OF 5200 (68.04%)**

Date of Issue: **July 17, 2014**
(see overleaf for Rules concerned to award of class)

A indicates 'ABSENT'

(*Courses registered but not counted for calculation of aggregate)



N. S. S. S. S.
CONTROLLER OF EXAMINATIONS