

Regd.No.

3848



**WINE YARD**  
TECHNOLOGIES

TM



## Diploma In Circuit Analysis & Design


This is to Certify that Mr / Ms TADURI DYNANDINI

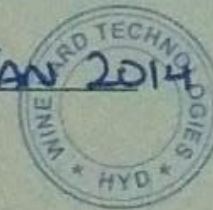
S/o.D/o T. VEERASWAMY has completed a design

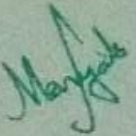
Project In The Domain Of Digital Design And Embedded Systems.

**DIPLOMA IN CIRCUIT ANALYSIS & DESIGN** is awarded to him/her, as he/she is able to Design Complex Circuit including analog, Digital and embedded based applications.

Date Of Issue : 27<sup>th</sup> JAN 2014

  
Manager



  
Director

**Corporate Office:** #8-3-903/8, G1 & G2, Tarakarama Estate, Lane Opp. RS Brothers, Ameerpet, Hyderabad.

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